

ECS
BROKERAGE
EVENT

IObundle, Lda

Semiconductor IP

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Company at a glance

- Lisbon, Portugal
- 7 employees (6 in R&I)
- R&I expenses close to 100%
- Semiconductor Intellectual Property (IP)
 - Digital systems design: hardware and embedded software
 - Python Framework for Efficient Design and Verification flow
- Applications
 - Audio and Imaging (ASRC, AC97, I2S-TDM, PNG)
- Markets
 - Consumer, Automotive, Broadcasting, Space & Defense

Company's Activities

- Previous experience with collaborative projects
 - A-IQ-Ready (partner, Chips JU, 2023-2026)
 - ODE4EC-PIV (partner, Chips JU, 2026-2028)
 - 6 NGI-funded projects, and more
- Interaction with one of the 3IAs or other ETPs
 - Visiting workshops/events

Collaboration Expectations

Collaborative project of interest

Chips JU, Horizon Europe, Eureka Clusters

Scope

Technology development

Project idea

Wearable Speech-to-Speech Translator (small size, low power edge AI)

Project impact

Wear a headset and speak in any language

Project consortium

We are searching for a chip design and systems partner for an ambitious and full-stack project

EU Semiconductor Sovereignty

