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NanolC: Accelerating beyond-2nm innovation across the ecosystem

Inge Asselberghs, imec

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The NanoIC pilot line is hosted and coordinated by

mec

and leverages existing and new tools in its fabs



Pilot line project partners















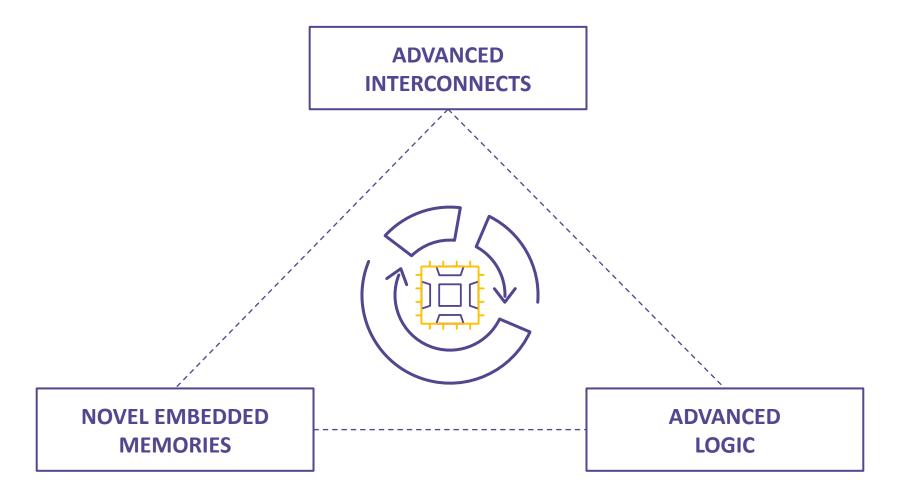








To enable beyond 2nm SoC, the NanolC pilot line offers collaborative R&D possibilities:







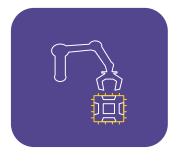
Progression requires extensive research into



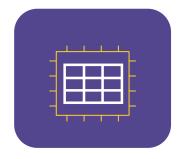
New materials



Process modules



Equipment

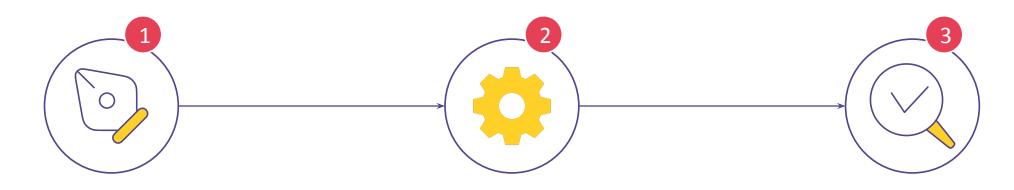


Advanced patterning capabilities





How to access?



Design

Access through PDK

Process

Baseline processes & module set-up at higher TRL level

Demonstrators

Validation of new capability/infrastructure





To enable the exploration of the potential impact of the pilot line technologies





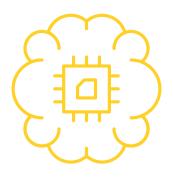
SYSTEM EXPLORATION PDKs

available to academia and designers (including SMEs and start-ups)





Building a skilled European semiconductor workforce



Opportunities for **PhD** students and **internships**



PDK workshops



Bootcamps



Expert courses





The NanolC pilot line offers opportunities for the entire ecosystem

START-UPs, SMEs







Join the NanolC journey...



Pilot line project partners























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