

**ECS
BROKERAGE
EVENT**

logiicdev

Let's work and lead together


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Lets Work and Lead Together

Brussels, 19 February 2025

 logiicdev GmbH

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logiicdev is Member of


Engage in the Digital Future

 **EPoSS**
European Association on
Smart Systems Integrati



Graz, Austria
 Area: 72sqm
 Sitting place: 6-8 people



4 Engineer (HDL, HW, FW, GUI)
 2 Management



'20 150K
 '21 100K
 '22 110K



80% invested in R&I



Facts & Figures



Product & Market:

1. FPGA based verification/prototype/evaluation.
2. Patented High speed Analog design for Digital twin.
3. Verification&Simulation –Macro-Die, Package, Chip & PCB.
4. AI based test equipment for Power supply.



Infrastructure:

- AI/Simulation server(256GB RAM)
- Component storage worth 8000€ (in stock)
- Testing Equipment: 15000€
- PC/Computer: 12000€



Company book value 75000K



Deepak V Katkoria
CEO & Founder



Thoe Hatzis
CTO



Ian Macnamara
Investor



Elena Mokina
Business Developer



Tanmay Badhan
AI & Python
Algorithm Developer



Sravani Thota
Power Electronics Engineer



Jakob Udier
Sales Consultant

The Team

That creates Value



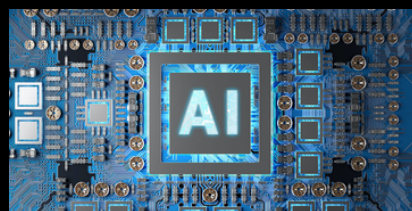
THE SOLUTION

OmniPower (OP)

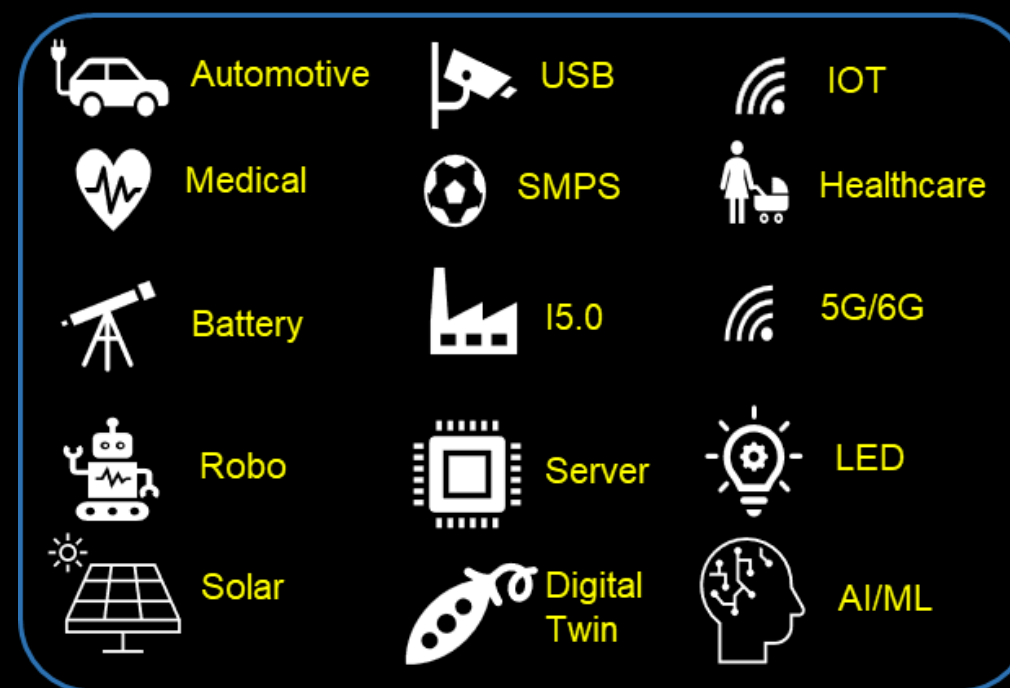
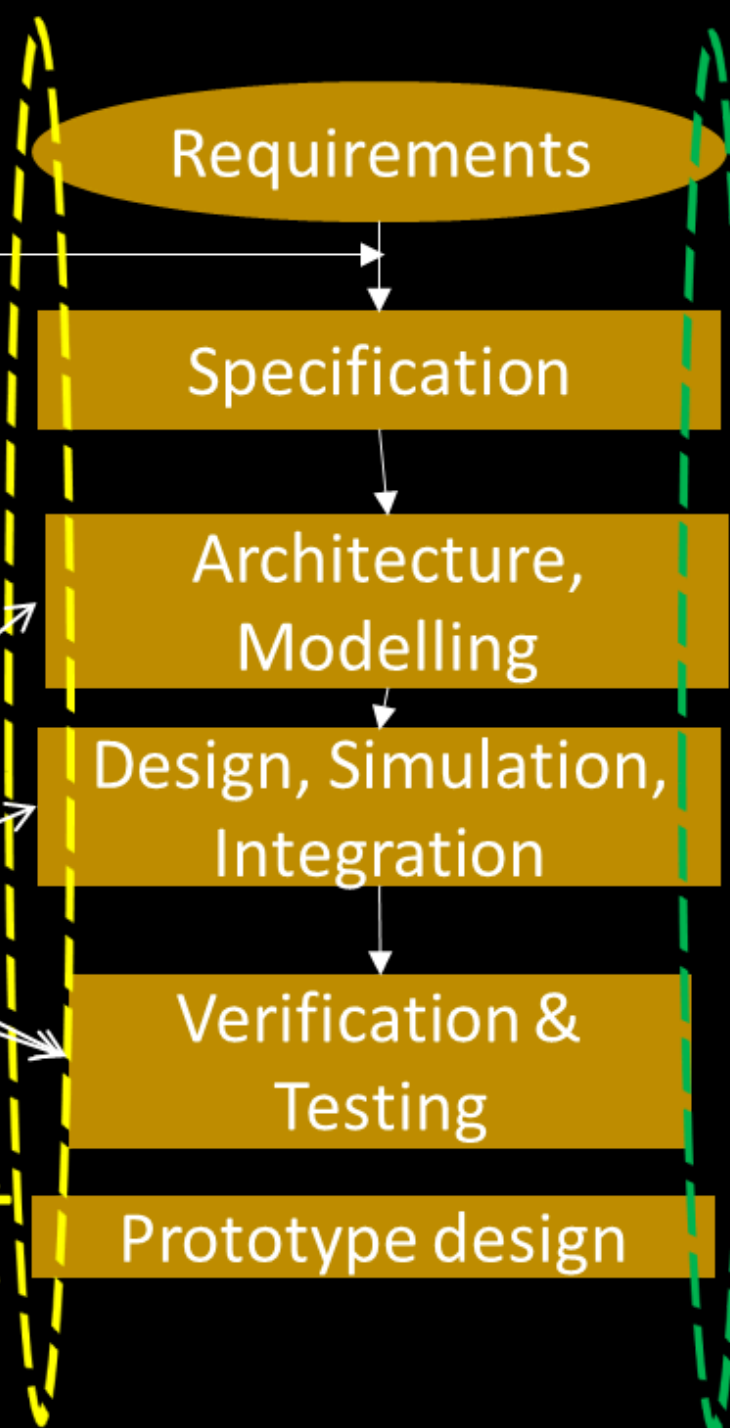
A dedicated motherboard explicitly designed for power applications across generation, transmission, and consumption. Much like the standard motherboard in computers, this patented technology is versatile and can be seamlessly integrated into any part of the power value chain. It supports digitization, AI/ML integration, digital twin, testing, grading, and predictive maintenance.

Collaborative projects:

In both personal and team capacities, we've successfully overseen and executed numerous collaborative projects within the realms of mobile, NFC, and PC design. Our dynamic team excels in software (SW), hardware (HW), firmware (FW), as well as Python-based algorithm development, showcasing a robust and comprehensive skill set.



Market study, Customer, Standards, constraints



EXPECTATIONS For Collaborations


Collaborative project of interest to you	<i>Chip JU, ECSEL, H2020, Eureka clusters, Eurostars, or other</i>
Scope	<i>Project management, technology development, verification, prototype, simulation and design</i>
Project consortium	<p><i>we are searching for leaders to offering expertise in developing, implementing and management from chip to system level design. Specilise in Analog, Digital and programmable logic design and testing. Our curiosity extends to diverse domains.</i></p> <p><i>We eagerly anticipate the opportunity to engage in in-depth discussions with you on these matters.</i></p>






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