

logiicdev

Lets makes Research in to killing product



MSc Deepak V Katkoria

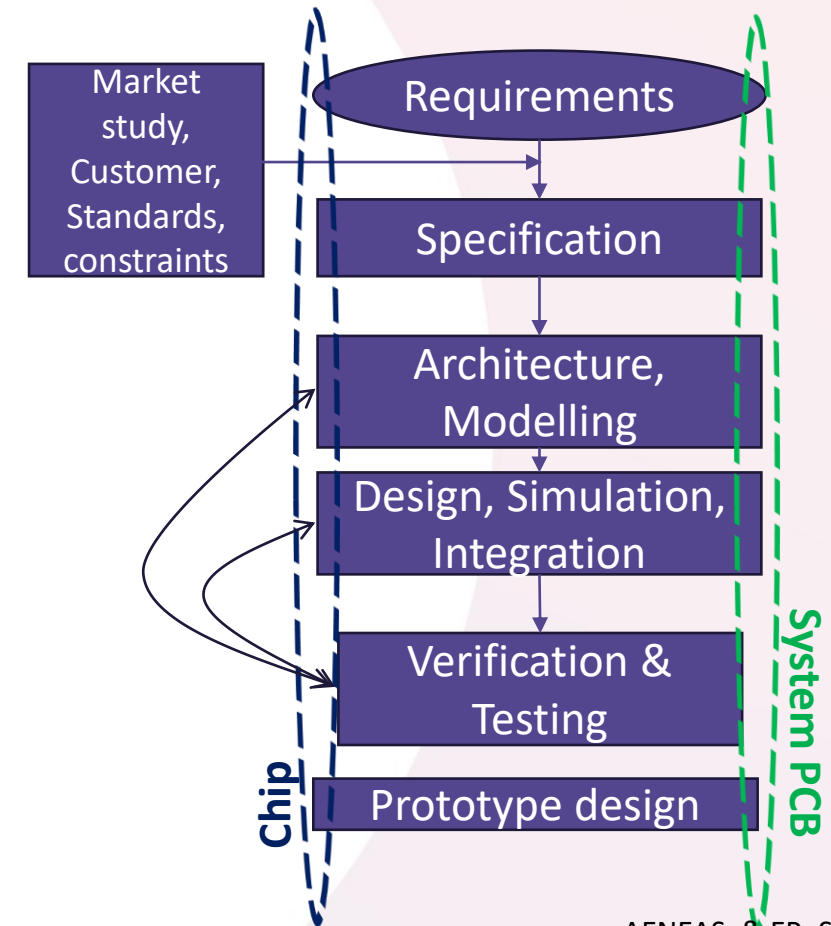
contact@logiicdev.eu

At a glance

- Graz, Austria
- 4 FT Engineer (HW, FW, GUI)
- '20 150K / '21 100K / '22 110K
- 80% invested in R&I
- Product & Market:
 - 1.FPGA based verification/prototype/evaluation.
 - 2.Patented High speed Analog design for Digital twin.
 - 3.Verification&Simulation –Macro-Die, Package, Chip & PCB.
 - 4.AI based test equipment for Power supply.

www.logiicdev.eu / contact@logiicdev.eu

ECS BROKERAGE EVENT



AENEAS & EPoSS Member



Lets work & lead together

Company's Activities

- Collaborative projects

In both personal and team capacities, we've successfully overseen and executed numerous collaborative projects within the realms of mobile, NFC, and PC design. Our dynamic team excels in software (SW), hardware (HW), firmware (FW), as well as Python-based algorithm development, showcasing a robust and comprehensive skill set.

- 3IAs or ETPs

- Work on SRAs / roadmaps

Collaboration Expectations



AENEAS & EPOSS Member



Lets work & lead together

Collaborative project of interest to you	<i>Chip JU, ECSEL, H2020, Eureka clusters, Eurostars, or other</i>
Scope	<i>Project management, technology development, verification, prototype, simulation and design</i>
Project consortium	<p><i>we are searching for leaders to offering expertise in developing, implementing and management from chip to system level design. Specilise in Analog, Digital and programmable logic design and testing. Our curiosity extends to diverse domains.</i></p> <p><i>We eagerly anticipate the opportunity to engage in in-depth discussions with you on these matters.</i></p>