

# Weeroc

High-end read-out-chip for photodetectors

Radiation Hardened analogue and mixed ASICs<sup>+</sup>

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+ CEO

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# Weeroc at a glance

- Villebon-sur-Yvette (Paris suburb), France
- 14 Employees, 12 FTE in R&D&I
- Turnover about 700kE par year
- Products : read-out chips for high-end photodetection (single photon, time of flight), photodetector modules
- Services : Turnkey solution for ASIC design and production



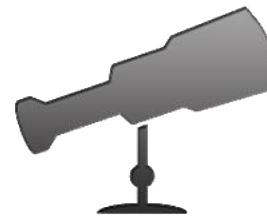
**Medical  
Imaging**



**Nuclear  
Industry**



**Quantum**



**Scientific  
Instrumentation**



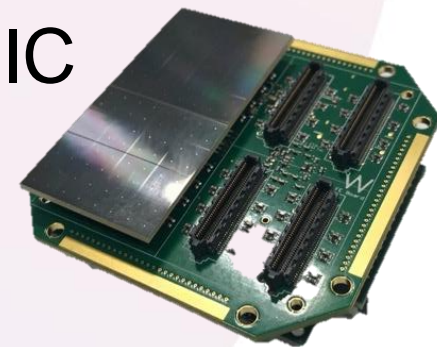
**Homeland  
Security**



**Aerospace  
Industry**

# Company's Activities

- Several EC funded projects since Weeroc inception
  - Medical imaging : PET electronic design in FP7 Trimage, PET module design in EIC Retimager
  - Quantum : front-end electronics in Random Power (TRNG)
  - Nuclear : provide front-end electronics in Temporal project (gamma camera)
- A whole photonics read-out chip family available of the shelf
- Several plug and play single-photon photodetector modules
- A large analog and digital IP library for fast and low-risk ASIC design



# Collaboration Expectations

Collaborative project	<i>Any kind Chips JU, Horizon Europe, Eureka Clusters</i>
Scope	<i>Technology development in photonics, quantum or space requiring ASIC design</i>
Project idea	<i>optional (specify application field)</i>
Project impact	<i>optional (specify the expected impact of your project)</i>
Project consortium	<i>Weeroc is not a project coordinator but can provide key technological solution based on ASIC</i>