

**ECS**  
BROKERAGE  
EVENT

# CISC Semiconductor

Subtitle explaining acronym

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# Company at a glance

## Adding TRUST in a connected world



CISC Semiconductor, established in 1999, is an international operating and highly awarded company that provides competitive and **innovative products** and **engineering solutions** to increase performance, conformance, interoperability, security and privacy in **communication technologies**.

Markets are **Automotive, RFID/NFC, Semiconductors** and **Wireless Communication**.

Headcount worldwide: **30**

# SOME FACTS ABOUT US



**1500+**  
PROJECTS



**120**  
COUNTRIES



**80+**  
JOINED R&D PROJECTS



**38**  
PATENTS



**25**  
YEARS OF EXPERIENCE

CARCONNECTIVITY  
consortium®



SILICONALPS

RAIN  
RFID  
CHARTER MEMBER

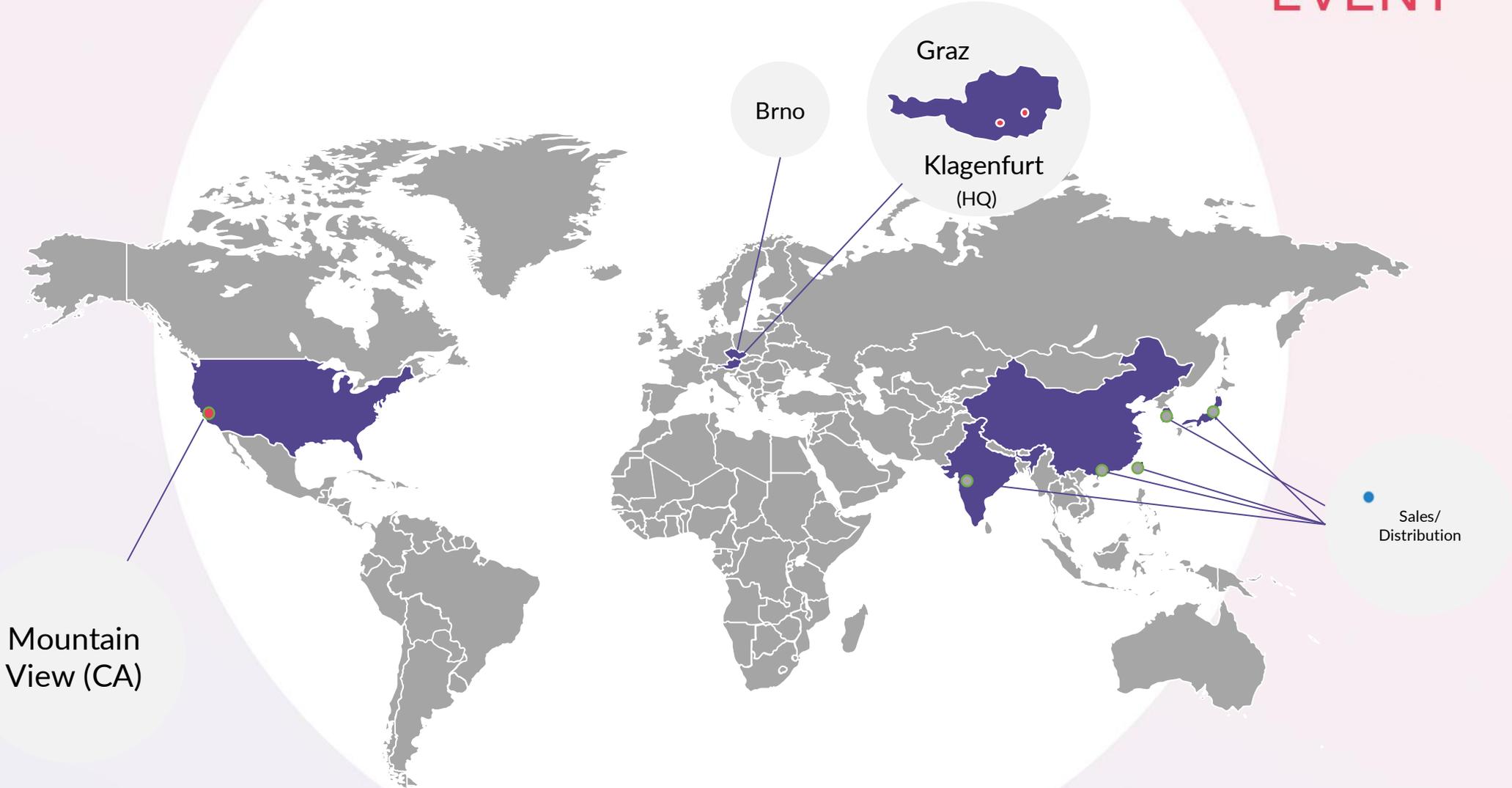


**15**  
MEMBERSHIPS



**8**  
AWARDS

# WHERE YOU CAN FIND US



# What we do – R&D references

- More than 500+ engineering projects with world-wide (EU, US, CA, CN, JP, BR, KR, ...) customers successfully finished
  - 4 customers among 1st 30 of “Fortune 500” companies
  - 200+ high tech companies form broad customer base
- More than 80 joint international R&D projects
  - Several 100 person years R&D investment
- 100% of R&D staff hold at least a master degree

# Company's Activities

- Current international collaborative projects:
  - HORIZON EUROPE project “**REFORM**” – Info under <https://www.reform-project.eu/>
  - ECSEL JU project “**TRANSACT**” – Info under <https://transact-ecsel.eu>
  - ECSEL JU project “**OPEVA**” – Info under <https://opeva.eu/>
  - ECSEL JU project “**ENERGY ECS**” – Info under [www.energyecs.eu](http://www.energyecs.eu)
  - Chips JU project “**AgrarSense**” – Info under <https://www.agrarsense.eu>
  - Chips JU project “**AIMS 5.0**” – Info under <https://aims50.eu>
- Interaction existing with one of the 3 Industry Associations or other ETPs
  - Regular visits of workshops and events
  - Regular contributions to **ECS SRA since 2020 - Mobility Chapter**
  - Member of **INSIDE**
  - Member of **ESBS Austria**
  - Shareholder Silicon Alps Cluster GmbH

# Collaboration Expectations

Collaborative project of interest to you	<i>Chips JU, Horizon Europe, Eureka Clusters and Eurostars</i>
Scope	<i>Technology deployment and new market access</i>
Project idea	
Project impact	
Project consortium	<i>we are offering expertise in embedded systems, communication and security technologies and search for partners for collaborative R&amp;D</i>
<i>Other information (e.g. project budget, duration)</i>	<ul style="list-style-type: none"> <li><i>• Min 24PM / year contribution</i></li> <li><i>• strong market relevant use cases expected</i></li> <li><i>• min 18 month duration</i></li> </ul>

# CISC Semiconductor's passion for advanced microcontrollers

15 years **experienced engineering** with the  
**Bosch GTM - Generic Timer IP Module**

Modelling and Simulation for concept engineering  
and verification

Visit us at  
[www.CISC-Semiconductor.com/GTM](http://www.CISC-Semiconductor.com/GTM)





secure configuration and wireless power-up for  
test bed sensor nodes

A photograph of industrial machinery, likely a semiconductor fabrication plant. The equipment consists of several vertical cylindrical chambers, each wrapped in white insulation. Each chamber is equipped with various sensors and control units, including orange and black electronic modules and red-handled valves. Wires and cables are connected to the equipment. The background shows a tiled wall and more industrial structures.

cybersecurity for industrial  
applications

A close-up, low-angle shot of a white industrial robot arm in a factory setting. The arm is positioned over a conveyor belt with yellow and white components. The background is a blurred factory floor with workers and machinery, creating a sense of a busy industrial environment.

secure wireless communication, OTA, data capturing and hardware-security based access for smart production



RAIN RFID and NFC test systems for hardware development, application set up and series production